


APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

Title of Invention	STRUCTURES AND METHODS FOR MANUFACTURING OF DISLOCATION FREE STRESSED CHANNELS IN BULK SILICON AND SOI CMOS DEVICES BY GATE STRESS ENGINEERING WITH SiGe AND/OR Si:C		
Application Type :		regular, utility	
Attorney Docket Number :		FIS920030375	
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as our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

Publication Information:

Suggested Figure for Publication -
Suggested Classification -
Suggested Technology Center -
Total Number of Drawing Sheets - 9

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